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PTO/SB/08B (10-01)

Approved for use through 10/31/2002. OMB 0651-0031

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application Number	10/726,490
		Filing Date	December 4, 2003
		First Named Inventor	Zi-Ping Chen et al.
		Group Art Unit	2836
		Examiner Name	SIRCUS, Brian
		Attorney Docket Number	IT0020-CIP
Sheet	2	of	2

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
CAB		M.D. Ker, "Lateral SCR Devices with Low-Voltage High-Current Triggering Characteristics for Output ESD Protection in Submicrom CMOS Technology", IEEE Trans. Electron Devices, Vol 45, pp. 849-860, April, 1998	<input type="checkbox"/>
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Examiner Signature		Date Considered	23 NOV 05
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